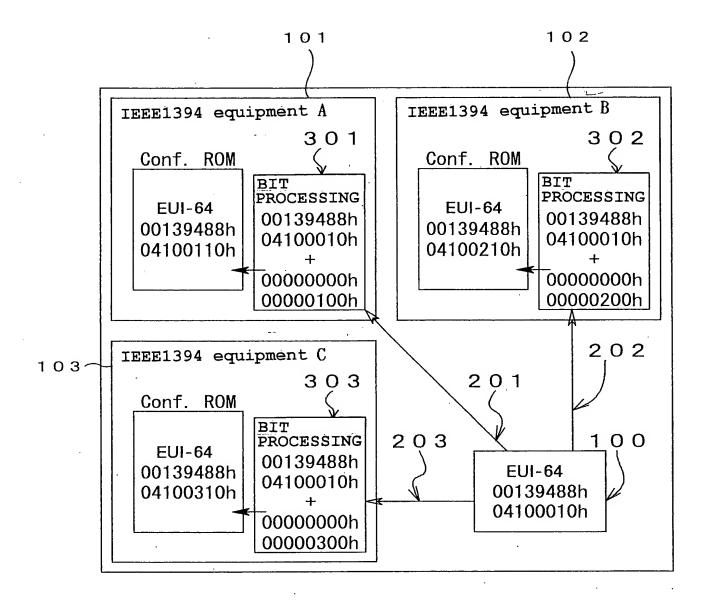
Fig. 1



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Fig. 2

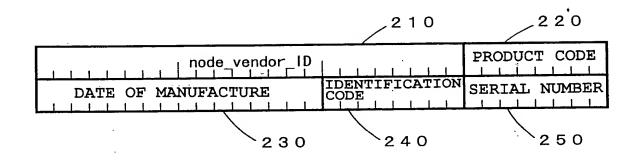


Fig. 3

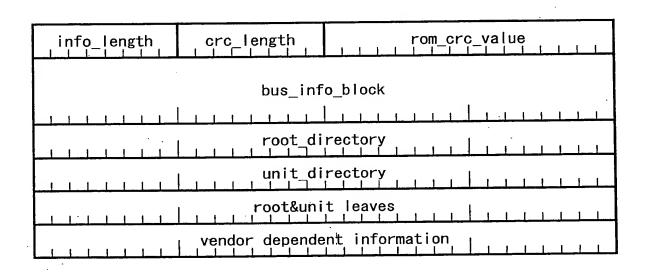


Fig. 4

"1" (31h)	″3″ (33h)	″ 9″ (39h)	"4" (34h)
D D D D reserved	cyc_clk_acc	max_rec		reserved
node_vendor_id				chip_id_hi
chip_id_lo				

Fig. 5

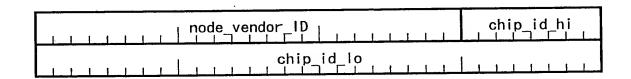


Fig. 6

